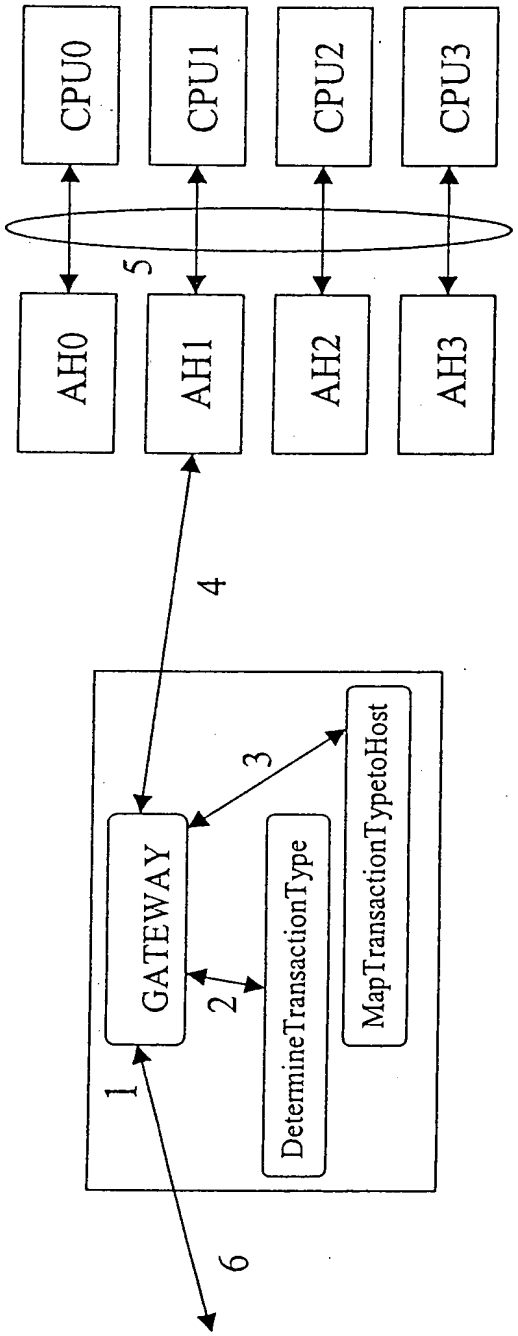
CPU0
CPU1
CPU2
CPU3
5
AH0
AH1
AH2
AH3
4
3
GATEWAY
2
DetermineTransactionType
MapTransactionTypetoHost
1
6

Figure 1

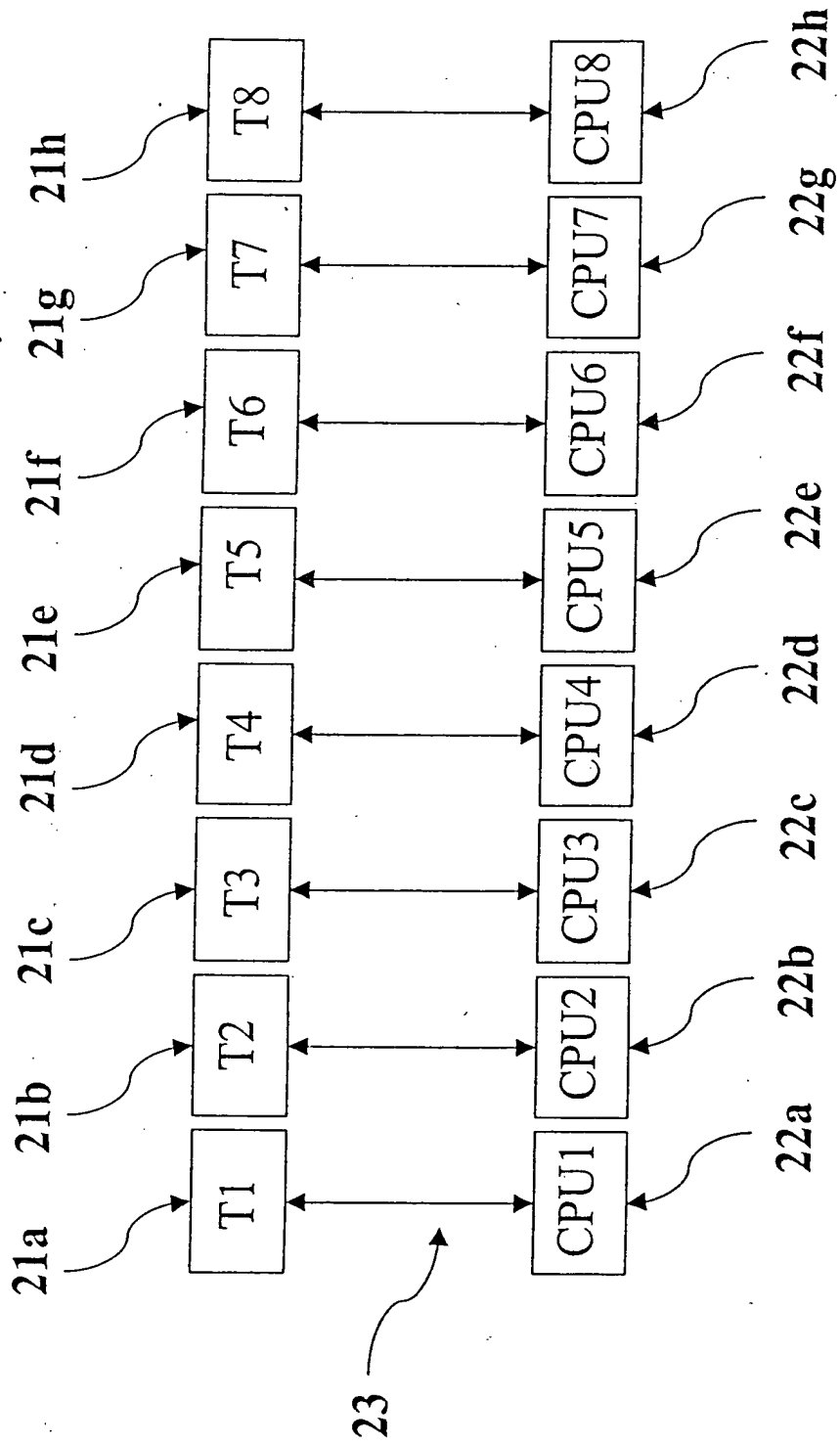
T1
T2
T3
T4
T5
T6
T7
T8
CPU1
CPU2
CPU3
CPU4
CPU5
CPU6
CPU7
CPU8
21a
21b
21c
21d
21e
21f
21g
21h
22a
22b
22c
22d
22e
22f
22g
22h
23

Figure 2

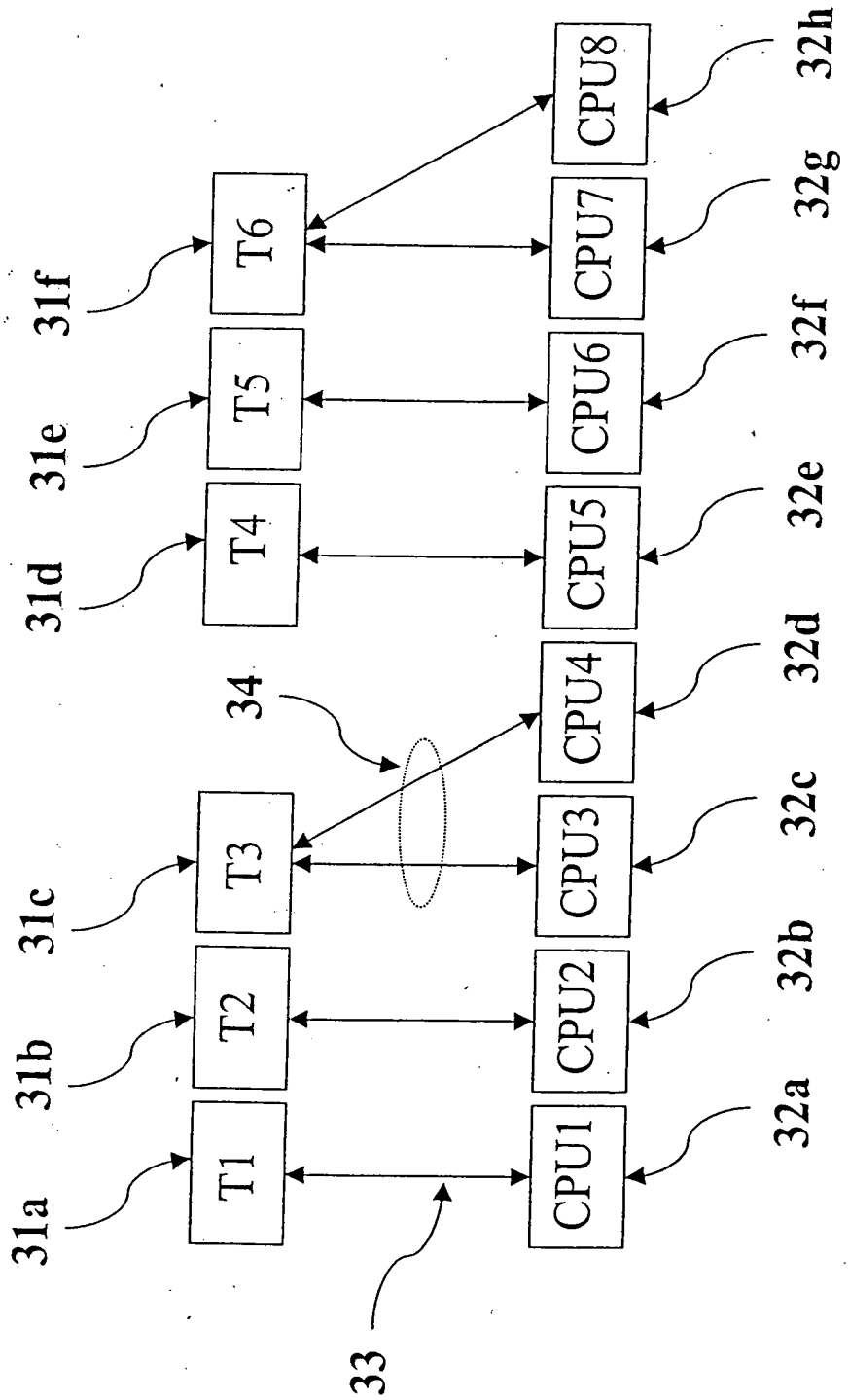
T1
T2
T3
T4
T5
T6
CPU1
CPU2
CPU3
CPU4
CPU5
CPU6
CPU7
CPU8
31a
31b
31c
31d
31e
31f
32a
32b
32c
32d
32e
32f
32g
32h
33
34

Figure 3

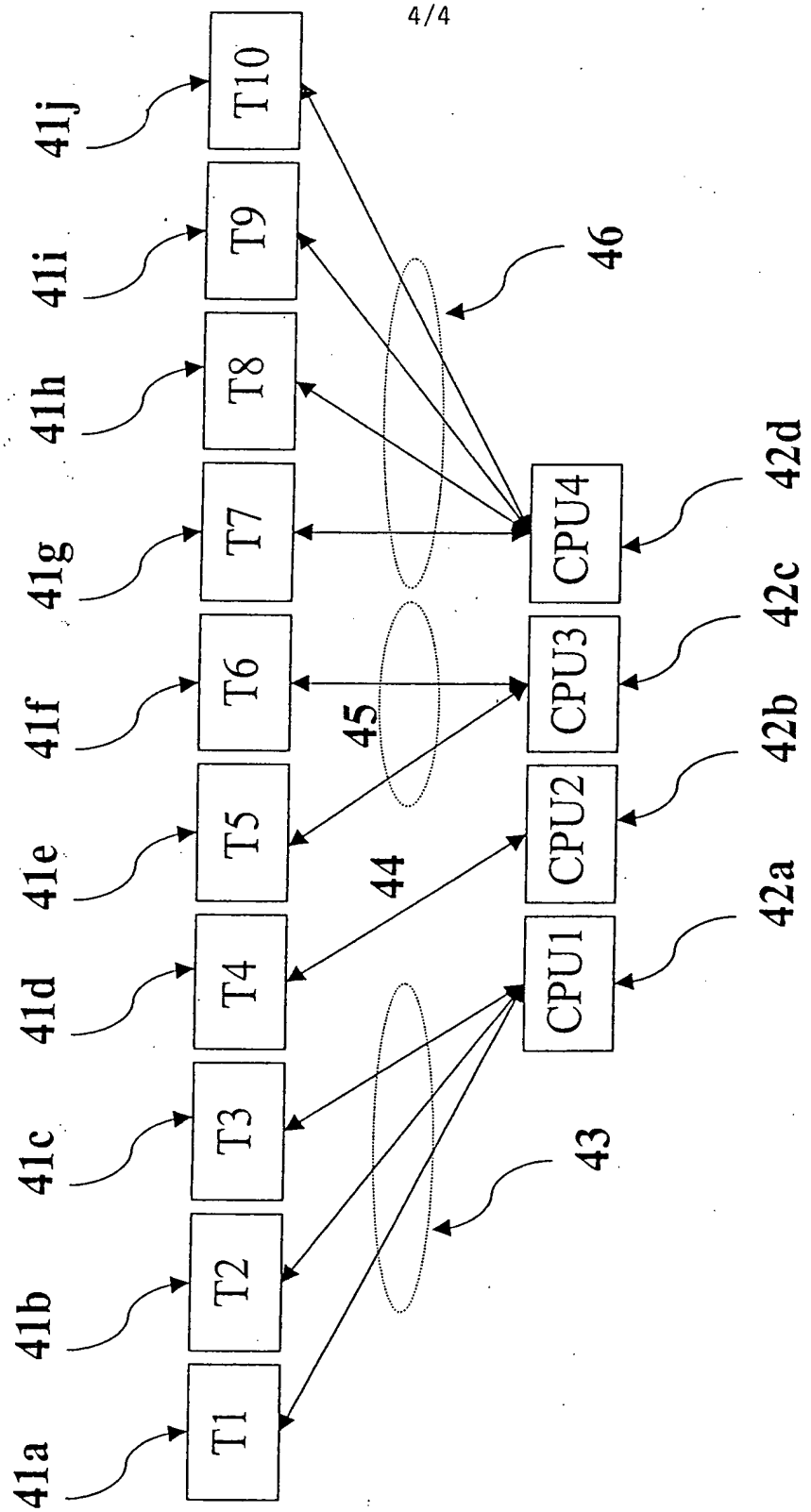
41a
41b
41c
41d
41e
41f
41g
41h
41i
41j
T1
T2
T3
T4
T5
T6
T7
T8
T9
T10
43
44
45
46
CPU1
CPU2
CPU3
CPU4
42a
42b
42c
42d

Figure 4